

Computer Applications in Power Electronic Systems

VIVEK KUMAR¹, VISHAL V. MEHTRE²

¹ Student, Department of Electrical Engineering, Bharati Vidyapeeth (Deemed to be) University, College of Engineering, Pune India

² Assistant Professor, Department of Electrical Engineering, Bharati Vidyapeeth (Deemed to be) University, College of Engineering, Pune India

Abstract- *Computer Applications in Power System is place an important role to verify the system performance and real time monitoring systems. Most of the Electrical system uses LABVIEW and other software to monitor real time electrical transition. In this paper we are focusing on Power Electronic Conversion system. Power Electronic System is easily simulated using MATLAB Software. Here we are giving easy simulation of Matrix converter and its result. Clean power development in power electronic system is an emerging area of research in Electrical Engineering. Most of the power electronic system introduces harmonics in power system. Elimination of harmonic is a tedious process. Different control strategy should be developed to reduce the harmonic. In Rectifier fed Inverter system has many disadvantages because of DC link filter. Matrix converter avoids this DC link filter and output approaches input. In this paper we have simulated Single phase Matrix Converter. Computer based simulation using MATLAB and Real Time implementation using PIC89C51 validate its originality. Finally, we have compared different values of Harmonics.*

Indexed Terms- *Matrix Converter, Clean Power, Rectifier fed Inverter, Harmonics, DC link filter, PIC89C51, Computer Applications.*

I. INTRODUCTION

Total Harmonic Distortion (THD) is the most common power quality index to describe the quality of power electronic converter. In general, all the output voltage of power electronic converters is not purely sinusoidal. For inverter application, THD represents how close the ac output waveform with pure sinusoidal waveform. A High-quality matrix converter system should have low THD. Various study has been made

on harmonic losses at electrical machine, it reveals that, presence of harmonic current in winding causes an increased copper loss [1]. The stator copper loss on a non-sinusoidal supply is proportional to the square of the total rms current [2]. The core loss in the machine is increased by the presence of harmonics in the supply voltage and current. Magnitude of harmonic loss obviously depends upon the harmonic content of the motor voltage and current. Large harmonic voltage at low harmonic frequencies causes significantly increased machine loss and reduced efficiency [3]. Many theoretical studies have been investigated on matrix converter but have found very few practical applications in power electronics. Matrix converter has been known to offer an "all silicon" solution for AC-AC conversion, removing the need for reactive energy storage components used in conventional rectifier-inverter based system. Its topology was first proposed by Gyugyi in 1976[1]. The circuit is arranged such that any output lines of the converter can be connected to any input lines. Obviously, published studies have mainly dealt with three- phase circuit topologies. The main purpose of this paper is to describe successful implementation of the SPMC that had been conceived, designed and realized by synthesizing the output using the well-known SPWM technique [2]. In this work, the micro controller was used to produce the sinusoidal pulse width modulation (SPWM) switching with SCR's as the SPMC power switching device. Prior to hardware implementations, simulations were performed to predict the behavior. A laboratory model test-rig of the SPMC was then constructed to perform investigation [3], [4]. Industrial application of matrix converter is still very limited because of some practical issues like common mode voltage effects, high susceptibility to input power disturbances and low voltage transfer ratio [5]. In order to extend the horizon of matrix converter into several distributed power sources

application, the objective of this research work is to simulate practical matrix converter. Objective of this research is to propose a PWM strategy to reduce THD, which is reported to R. These results are compared with various operating frequencies. Single phase matrix converter is developed and simulated using MATLAB Simulink and this result is verified using real time system.

II. DIRECT AC TO AC CONVERTER

The AC/AC converters are commonly classified into indirect converter which utilizes a dc link between the two ac systems and direct converter that provides direct conversion. Indirect converter consists of two converter stages and energy storage element, which convert input ac to dc and then reconverting dc back to output ac with variable amplitude and frequency [9]. The operation of these converter stages is decoupled on an instantaneous basis by means of energy storage element and controlled independently, so long as the average energy flow is equal. Figure1 shows single phase matrix converter arrangement.

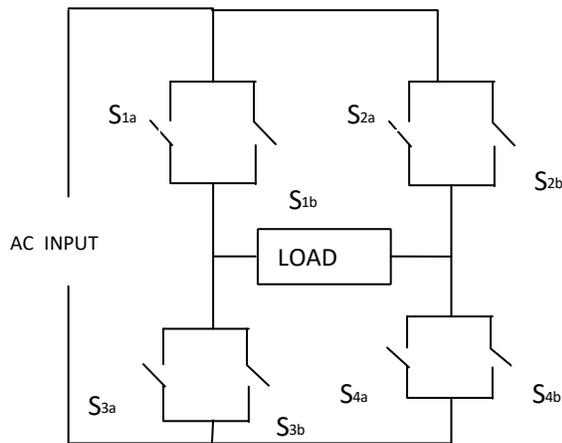


Fig. 1. Single Phase Matrix Converter: Where S1a refers Bidirectional switch

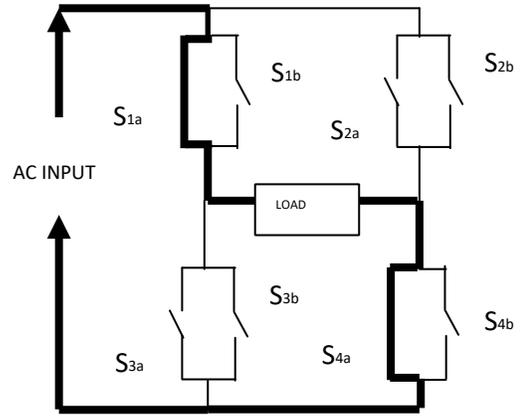


Fig. 2. Single Phase Matrix Converter: Where S1a and S4a Switched On (Positive Half Cycle)

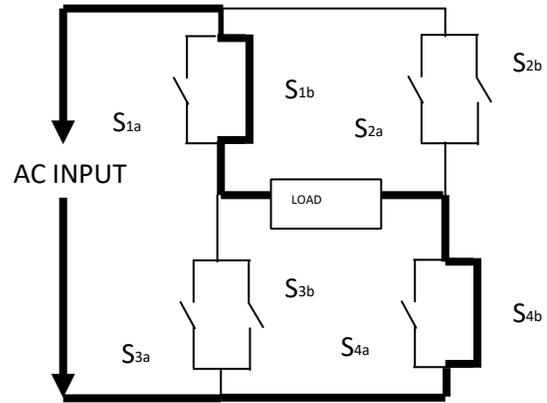


Fig. 3. Single Phase Matrix Converter: Where S1b and S4b Switched On (Negative Half Cycle)

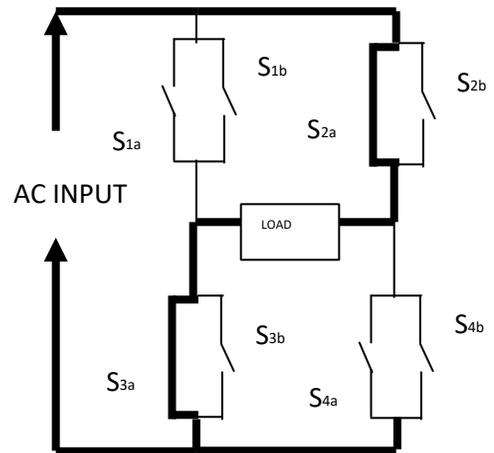


Fig. 4. Single Phase Matrix Converter: Where S2a and S3a Switched On (Positive Half Cycle)

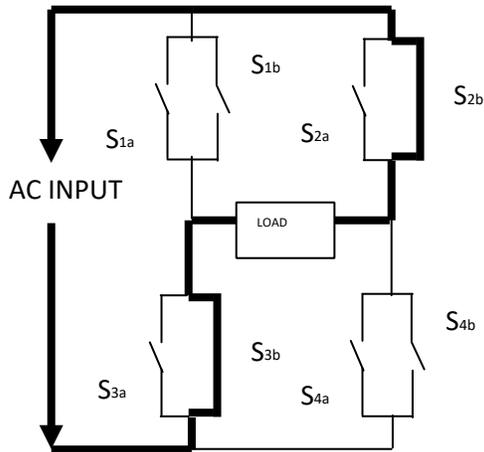


Fig. 5. Single Phase Matrix Converter: Where S2a and S3a Switched On (Negative Half Cycle)

The Matrix Converter requires a bidirectional switch capable of blocking voltage and conducting current in both directions. Unfortunately, there is no discrete component that fulfils these needs. To overcome this problem the common emitter anti-parallel IGBT [6], diode pair is used. Diodes are in place to provide reverse blocking capability to the switch module.

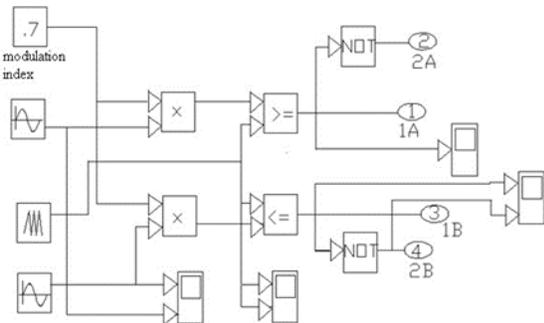


Fig. 6. Generation Sinusoidal pulse width modulation

Figure 2 to Figure 5 shows that Operation of matrix converter for 4 modes of operation. Figure 6 shows that generation of Sinusoidal Pulse width modulation using MATLAB. This may be implemented using C language. In this research we are using special purpose IC which has inbuilt function to generate Sine PWM.

III. DIFFERENT MODULATION SCHEME

A sinusoidal ac voltage source having an amplitude V_1 and angular frequency ω_1 ($2\pi f_1$) is connected to the input terminal of matrix converter. This is applied

sinusoidal voltage is converted into an output voltage with amplitude of V_0 and angular frequency ω_0 ($2\pi f_0$), which is applied to the load. Upper limit of the range of variation of the output frequency lies at a point lower than the input frequency [7].

$$V_{ABC} = K_i e^{j(\omega_i t + \Phi_i)} \quad (1)$$

$$V_{UVW} = K_0 e^{j(\omega_0 t + \Phi_0)} \quad (2)$$

Control principle(S (t))

$$S(t) = \frac{K_0 e^{j(\omega_0 t + \Phi_0)}}{K_i e^{j(\omega_i t + \Phi_i)}} \quad (3)$$

$$j(\omega_i t + \Phi_i)$$

$K_i e$

Maximum voltage transfer ratio 50% is possible in Venturini

Algorithm. An improvement in the achievable voltage ratio to

$$\frac{\sqrt{3}}{2}$$

(for 87%) is possible by adding common mode voltage to the

target output. We can investigate these voltage

transformation ratio based on following theoretical analysis. There are different modulation techniques

available from Venturini invention (1980) [9]. Implementation of venturini algorithm is difficult

calculation. We are looking forward towards simple algorithm and improved voltage transformation ratio

[10]. Following switching equations will explain about output voltage relationship and review of switching

techniques. Equation 1, 2 and 3 shows that basic modulation development.

3.1 Venturini modulation method (Venturini first

method) $m_{kj} = T_{tsepkj} \cdot \frac{1}{3} [1 - 2v_{v2} k_{imv} j]$, (4)

for $k=A, B, C$ and $j=a, b, c$

Voltage transformation ratio is 50%. From equation (4) second term $q=1/2$.

3.2 Venturini Optimum method (Venturini Second method)

Employs common mode addition and maximum transformation ratio is 87%. This is also known as

displacement factor control [11]. Displacement factor control can be introduced by inserting a phase shift

between the measure input voltages and inserted voltage (v_k). Equation 5 shows that relationship

between modulation index and maximum voltage transformation. $m_{kj} = T_{tsepkj} \cdot \frac{1}{3} [1 - 2v_{v2} k_{imv} j - 34q_3 \sin(\omega_i t - \omega_k) \sin(3\omega_i t)]$, (5)

For $k=A, B, C$ and $j=a, b, c$
 $\beta_k = 0, 2\pi/3$ for $k=A, B, C$

3.3 Scalar Modulation method:

Actuation signals are calculated directly from measurement of input voltages. Voltage transformation ratio is 87%.

$$m_{kj} = \sqrt{3} [1 + 2v_2^k v_1^j - 2^2 \sin(\omega t - \omega_k) \sin(3\omega t)] \quad (6)$$

This method yields virtually identical switch timings to the optimum Venturini method [12]. The maximum output voltage ($q = \frac{\sqrt{3}}{2}$) are identical. Only the difference between the methods is that the right most term addition is taken pro rata with q in the Venturini method. Equation 6 shows that relationship between modulation index and maximum voltage transformation.

3.4 SPVM

Space vector pulse width modulation is applied to output voltage and input current control. This method is advantage because of increased flexibility in choice of switching vector for both input current and output voltage control can yield useful advantage under unbalanced conditions.

3.5 Indirect Modulation method

This method aims to increase the maximum voltage ratio above 86.6% limit of other methods.

$$v_o = (A v_i) B = \frac{3K^A K^B V_{im}}{2} \cos(\omega t - 2\pi/3) \quad (7)$$

Voltage ratio $q = 3K_A K_B / 2$. Clearly A and B modulation steps are not continuous in time as shown above.

$$K_A = 2\sqrt{3} V_{im} / \Pi \quad (8)$$

$$K_B = 2 / \Pi \quad (9)$$

$$\text{Then, } q = 6\sqrt{3} / \Pi^2 = 105.3\% \quad (10)$$

The voltage output is greater than the previous method. For the values $q > 0.866$, the mean output voltage no longer equals the target output voltage in each switching interval. This inevitably leads to low frequency distortion in the output voltage and /or the input current compared to other methods with $q < 0.866$. For $q < 0.866$, the indirect method yields very

similar results to the direct methods. Equation 7 to 10 shows that relationship between modulation index and maximum voltage transformation [13], [14]. We are using Sinusoidal pulse width modulation is used to control Switching Devices.

IV. REAL TIME ENVIRONMENT USING MATLAB AND PIC CONTROLLER

Figure 7 shows Power and Control circuit implementation in real time environment.

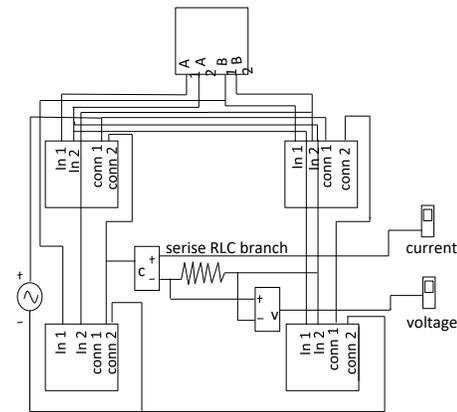


Fig. 7. Simulation of Single Phase Matrix Converter Using MATLAB

A Simple circuit is developed using available components in the laboratory. The control circuit consists of Diode Bridge, counter circuit and amplifier circuit. When 12V AC supply is given, the bridge rectifier converts it into 12V DC. The bridge rectifier consists of IN 4007 Diode. The positive series regulator (7805) steps down the output of bridge rectifier to 5V. This 5V voltage is given as the supply to the counter circuit which gives square pulse as output. The counter circuit consists of synchronous counter (IC). For each half cycle of the input voltage the counter produces the square pulses to trigger the thyristors correspondingly. This output is given to amplifier circuit. The amplifier circuit consists of four Darlington pair amplifiers which amplify the current in order to trigger the corresponding SCRs. The Darlington pair consists of transistor (SL 100). The output of each Darlington pair is given to a pulse transformer (1:1:1). The voltage in the primary of the transformer is induced in the secondary which will trigger the SCRs in the sequence mentioned above. Above circuit arrangements are shown in Figure 8.

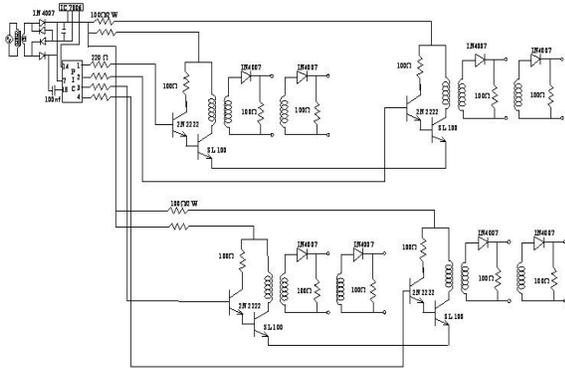


Fig .8. Control Circuit

The micro controller that has been used for this research is PIC series. PIC micro controller is the first RISC based microcontroller fabricated in CMOS that uses separate bus for instruction and data. The additional advantage of CMOS has noise to immunity lesser than other fabrication techniques. Various micro controllers offer different kinds of memories. EEPROM, EPROM, FLASH etc., some of the memories of which FLASH is the most recently developed. Technology that is used in PIC16F877A is FLASH technology, so that data is retained even when the power is switched OFF. Easy programming and erasing are other features of PIC16F877A. A simple algorithm to develop Sinusoidal Pulse Width Modulation is shown in Appendix A.

Above programs are developed using inbuilt PWM instruction.

V. RESULT AND ANALYSIS

First single-phase matrix converter is simulated for R load. This result is compared with Matlab Simulation results. Real time measurement is following the computer simulation. This proves that Matrix converter is a technology which is used to convert direct AC to AC conversion. This analysis can be done from Figure 9 and Figure 10. Next set of analysis is High voltage transformation ratio. This is indirectly proved here. If total harmonic is reduced then High voltage will be transformed to output side. Following section prove the above statement. For the sake of analysis we took only Output voltage.

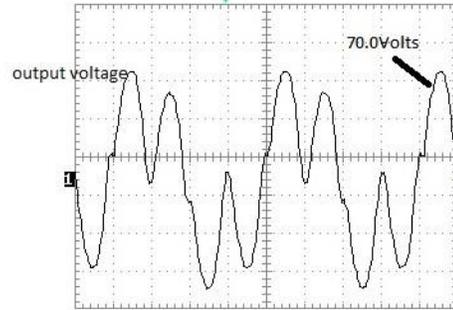


Fig. 9. Practical Output voltage for 25Hz (Measured using Scientific DSO)

Result is simulated for 25Hz output frequency. In case of 25Hz Output voltage is similar to that of Figure 9 (Practical measured value) and Figure 10 (Matlab simulation). But there is no change in shape of the output. But RMS value of output voltage and output current are varying. This may reduce the voltage transformation ratio of the converter. But our aim is fulfilled.

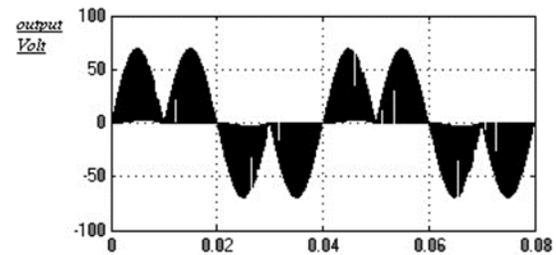


Fig.10. Output Voltage for 25Hz (Measured using computerized simulation-Matlab)

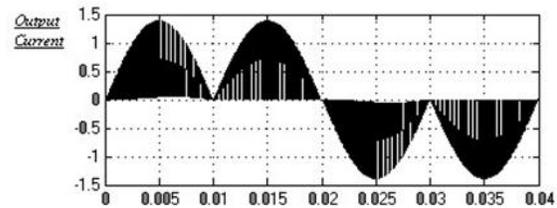


Fig. 11. Output Current 25Hz

Table 1 shows the open loop simulation of matrix converter with different load conditions. THD are measured for open loop Space vector control method. By changing the switching behavior of the inverter, the audible noise can also be influenced and therefore be minimized. Space Vector Modulation provides excellent output performance, optimized

efficiency, and high reliability compared to similar inverters with conventional Pulse Width Modulation.

Table 1 THD comparison R,RL and Motor load(Open loop simulation),without output filter

Parameter	R Load	RL Load	Motor Load
Switching Frequency	16KHz	16KHz	16KHz
Output THD	65.64%	22.05%	4.9%
Input THD	22.7%	22.59%	22.43%

CONCLUSION

In this paper a Single phase MC was proposed. Voltage control was implemented using Sinusoidal Pulse Width Modulation. For various values of frequency output is measured. Simulation results are verified with respect to hardware implementation. Safe commutation principle also implemented. This principle may be implemented for three phase matrix converter. Further this simulation may be extended to inductive load and better result may be implemented using space vector pulse width modulation. If we are very good at implementation of matrix converter control principle Phase Phasor vector control also possible. Important point to be noted that during the identification of sectors it is tedious process. It may be made simple if this algorithm is developed using Digital Signal Processor.

REFERENCES

[1] V. Ramanarayanan, Matrix converter, "Proceedings of national workshop on power electronics NWPE2006", NAMPET, IIT Kanpur.

[2] P.D.Ziogas, S.I.Khan and M.H. Rashid, "Analysis and design of forced commutated cycloconverter structures with improved transfer characteristics", IEEE transactions on Industrial Electronics, vol.IE-33, August 1986, 271-280.

[3] Patrick W. Wheeler, Jose Rodriguez, Jon C .Clare , Lee Empringham and Alejandro Weinstein,"Matrix Converters: A Technology

Review", IEEE Transactions on Industrial Electronics, Vol.49, No.2, April 2002, 282-285 [4] C. L. Weft and C. D. Schauder, "Theory and design of 30HP matrix converter", IEEE Transactions on Industrial Applications, Vol.28, No 3, PP 546-551.

[4] N. Mahedran and G. Gurusamy, "THD Analysis of Matrix converter fed load", Proceedings of International Conference on Power Electronics and Drive Systems (IEEE PEDS2009TAIWAN), Page(s): 828 – 832

[5] N. Nquyen-Quag, D. A. Stone, C.M. bingham and M.P. Foster, "Single phase matrix converter for radio frequency induction heating", IEEE SPEEDAM 2006, S18-S28.

[6] T. N. Wheeler, J. C. Clare, L.de Lillo and K. J. Bradley," A comparison of reliability of a matrix converter and a controlled rectifier inverter", EPE 2005, P1-P7.

[7] M. S. N. Romli, Z. Idris, A. Saparaon and M. K. Hamazah,"An area efficient sinusoidal Pulse Width Modulation(spwm)Technique for single phase matrix converter(spmc)", IEEE 2008, Page 1163-1168.

[8] S. Z. Mahammad Noor, R. Baharom, N. S. Damanhuri and

[9] M. K. Hamzah,"Single phase inverter with fully controlled regenerative capability using single phase matrix converter", IEEE 2008, page 934-939.

[10] Siti Zahiha Mohammad Noor, Mustafar Kamal Itamzah and Ahmaed Farid Abidin," Modelling and Simulation of a DC Chopper using Single phase matrix converter topology", IEEE PEDS 2005, Page 827-832.

[11] Mustafar kamal Hamzah, Mohamad Fadzil

[12] Saidan,"Application of single phase matrix converter topology in UPS circuit incorporating unity Power factor control", IEEE ICIEA 2006.

[13] Malcolm tabore, Cyril Spiteri Staines , Joseph, "Low cost three phase to single phase matrix converter", IEEE ICIT 2004, Page 474-479.

[14] Zahirudin Idris, Siti Zaliha Mohammed Noor, Mustafar Kamal hamzah," Safe Commutation

Strategy in single phase matrix converter”, IEEE PEDS 2005, Page 886-891.

- [15] N.Mahendran, Dr. G.Gurusamy, “Modelling and Simulagtion of Sine wave to Sine wave converter for AC drive-A technology reviw” Proceedings of the International Conference on ICMEE 2009,Chennai, India, 24 - 26 July 2009, (pp 285-289).